

it will be understood by those skilled in the art that various changes in form and details may be made therein without departing from the spirit and scope of the invention as defined by the appended claims.

What is claimed is:

1. An electronically addressable display comprising:  
a substrate having a surface;  
an electronically addressable display media printed on said surface of said substrate; and  
a plurality of analog or digital logic elements printed on said surface of said substrate, said plurality of printed logic elements matrix-addressing said printed display media.
2. The electronically addressable display of claim 1 wherein said display media is bistable.
3. The electronically addressable display of claim 1 wherein said substrate defines a plurality of apertures.
4. The electronically addressable display of claim 1 further comprising a plurality of address lines printed on said substrate, said address lines in electrical communication with said printed display media and said printed logic element.
5. The electronically addressable display of claim 1 wherein said printed logic element comprises a polymeric transistor.
6. The electronically addressable display of claim 1 wherein said printed logic element comprises an organic transistor.
7. The electronically addressable display of claim 1 wherein said printed logic element comprises a solution-deposited polymeric transistor.
8. The electronically addressable display of claim 1 wherein said printed logic element comprises an optoelectronic switch.

9. An electronically addressable display comprising:  
a plurality of substrates integrated to form a display ensemble, each of said substrates having a surface;  
an electronically addressable display media printed on said surface of each said plurality of substrates;  
a plurality of analog or digital logic elements printed on said surface of each of said plurality of substrates, said plurality of printed logic elements matrix-addressing said printed display media.
10. An electronically addressable display of claim 9 wherein said display media is bistable.
11. The electronically addressable display of claim 9 wherein each of said plurality of substrates define a plurality of apertures.
12. The electronically addressable display of claim 9 further comprising a plurality of address lines printed on each of said plurality of substrates, each of said plurality of address lines in electrical communication with each respective printed display media and printed logic element.
13. The electronically addressable display of claim 9 wherein said printed logic element comprises a polymeric transistor.
14. The electronically addressable display of claim 9 wherein said printed logic element comprises an organic transistor.
15. The electronically addressable display of claim 9 wherein said printed logic element comprises a solution-deposited polymeric transistor.
16. The electronically addressable display of claim 9 wherein said printed logic element comprises an optoelectronic switch.

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